

LCD MODULE SPECIFICATION

Customer: _____
Model Name: VI101GI5B5
Date: 2024/11/30
Version: V02

- Preliminary Specification
 Final Specification

For Customer's Acceptance

Approved by	Comment

Approved by	Reviewed by	Prepared by

Record of Revision

Version	Revise Date	Page	Content
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Contents

1. General Specifications	1
2. Pin Assignment	2
3. Operation Specifications	4
3.1. Absolute Maximum Ratings	4
3.2. Typical Operation Conditions	5
3.3. Current Consumption (White Pattern)	5
3.4. Power Sequence	6
3.5. LVDS Signal Timing Characteristics	7
3.4.1. AC Electrical Characteristics	7
3.4.2. Timing Table	8
3.4.3. LVDS Data Input Format	9
4. Optical Specifications	10
5. Reliability Test Items	14
7. General Precautions	15
7.4. Safety.....	15
7.5. Handling	15
7.6. Static Electricity	15
7.7. Storage.....	15
7.8. Cleaning.....	15
8. Mechanical Drawing	16
9. Package Drawing	18

1. General Specifications

No.	Item	Specification	Remark
1	LCD size	10.1 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	1280 × 3(RGB) × 800	
4	Display mode	Normally Black, Transmissive	
5	Dot pitch	0.0565(W) × 0.1695(H) mm	
6	Active area	216.96(W) × 135.60(H) mm	
7	Module size	229.46(W) × 149.1(H) × 2.5(D) mm	Note 1
8	Surface treatment	HC	
9	Color arrangement	RGB-stripe	
10	Interface	LVDS	
11	View direction(Gray Inversion)	free	
12	Panel power consumption	0.7W(Typ.)	Note 2
13	Weight	205 (Typ.)	
14	inversion	1+2line	
15	IC	S-IC: ILI6192, G-IC:ILI5801	

Note 1: Refer to Mechanical Drawing.

Note 2: Power consumption is white pattern

2. Pin Assignment

A 40pin connector is used for the module electronics interface. This model used 196479-40041-3 and FP225H-040611M.

Pin No.	Symbol	I/O	Function	Remark
1	NC	---	No connection	
2	VDD	P	Power Supply	
3	VDD	P	Power Supply	
4	NC	---	Only INX test	
5	NC	---	Only INX test	
6	NC	---	Only INX test	
7	GND	P	Ground	
8	Rxin0-	I	-LVDS Differential Data Input	R0-R5, G0
9	Rxin0+	I	+LVDS Differential Data Input	
10	GND	P	Ground	
11	Rxin1-	I	-LVDS Differential Data Input	G1~G5, B0,B1
12	Rxin1+	I	+LVDS Differential Data Input	
13	GND	P	Ground	
14	Rxin2-	I	-LVDS Differential Data Input	B2-B5,HS,VS, DE
15	Rxin2+	I	+LVDS Differential Data Input	
16	GND	P	Ground	
17	RxCLK-	I	-LVDS Differential Clock Input	LVDS CLK
18	RxCLK+	I	+LVDS Differential Clock Input	
19	GND	P	Ground	
20	Rxin3-	I	-LVDS Differential Data Input	R6, R7, G6, G7, B6, B7
21	Rxin3+	I	+LVDS Differential Data Input	
22	GND	P	Ground	
23	NC	---	Only INX test	
24	NC	---	No connection	
25	GND	P	Ground	
26	NC	---	No connection	

27	NC	---	No connection	
28	NC	---	No connection	
29	AVDD	P	Power for Analog Circuit	
30	GND	P	Ground	
31	LED-	P	LED Cathode	
32	LED-	P	LED Cathode	
33	NC	---	Only INX test	
34	NC	---	No connection	
35	VGL	P	Gate OFF Voltage	
36	NC	---	No connection	
37	NC	---	No connection	
38	VGH	P	Gate ON Voltage	
39	LED+	P	LED Anode	
40	LED+	P	LED Anode	

I: input, O: output, P: Power

3. Operation Specifications

3.1. Absolute Maximum Ratings

Item	Symbol	Values		Unit	Remark
		Min.	Max.		
Power voltage	VDD	-0.3	4	V	
	AVDD	-0.3	18	V	
	V _{GH}	-0.3	42	V	
	V _{GL}	-20	0.3	V	
	V _{GH} -V _{GL}	12	40	V	
Operation Temperature	T _{OP}	-10	60	°C	
Storage Temperature	T _{ST}	-20	70	°C	

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

3.2 Typical Operation Conditions

(Note 1)

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	VDD	2.3	2.5	2.7	V	Note 2
	AVDD	8.0	8.2	8.4	V	
	V _{GH}	12	12.5	13	V	
	V _{GL}	-13.5	-13	-12.5	V	
Input signal voltage	VCOM	-	-	-	V	Note 3
Ripple voltage	VRP	-	-	150	mV	Note 4
Input logic high voltage	V _{IH}	0.8 VDD	-	VDD	V	
Input logic low voltage	V _{IL}	0	-	0.2 DV _{DD}	V	

Note 1: Be sure to apply VDD and AVDD to the LCD first, and then apply V_{GH} and V_{GL}.
 Note 2: VDD setting should match the signals output voltage of customer's system board
 Note 3: Auto Vcom, no Vcom voltage required.
 Note 4: Including signal: VDD、AVDD、V_{GH}、V_{GL}

3.3. Current Consumption (White Pattern)

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Current for Driver	I _{GH}	0.30	0.68	1.00	mA	V _{GH} =12.5V
	I _{GL}	0.30	0.63	1.00	mA	V _{GL} = -13V
	I _{VDD}	18.1	32.5	44.6	mA	V _{DD} =2.5V
	I _{AVDD}	46.7	72.1	115.9	mA	AV _{DD} =8.2V

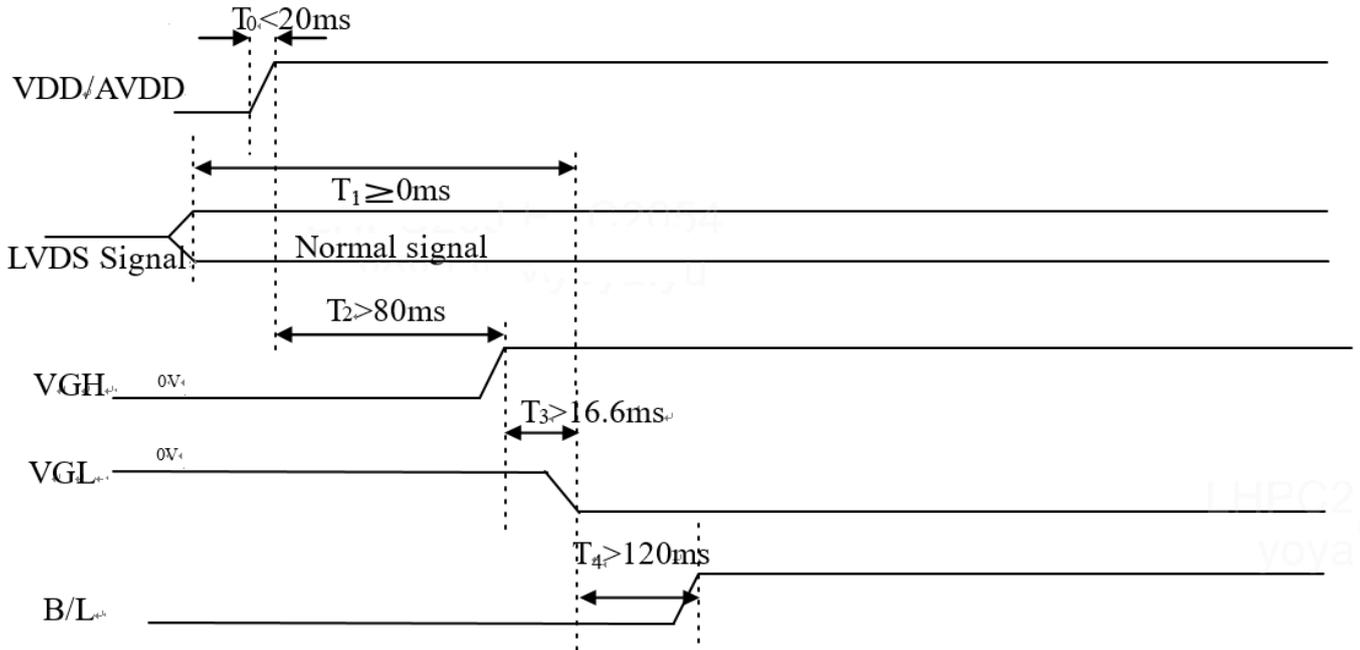
3.3.1 Backlight Driving Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Voltage for LED backlight	V _L	8.4	9.3	10.5	V	Note 1
Current for LED backlight	I _L	198	200	205	mA	
LED life time	-	20,000	-	-	Hr	Note 2

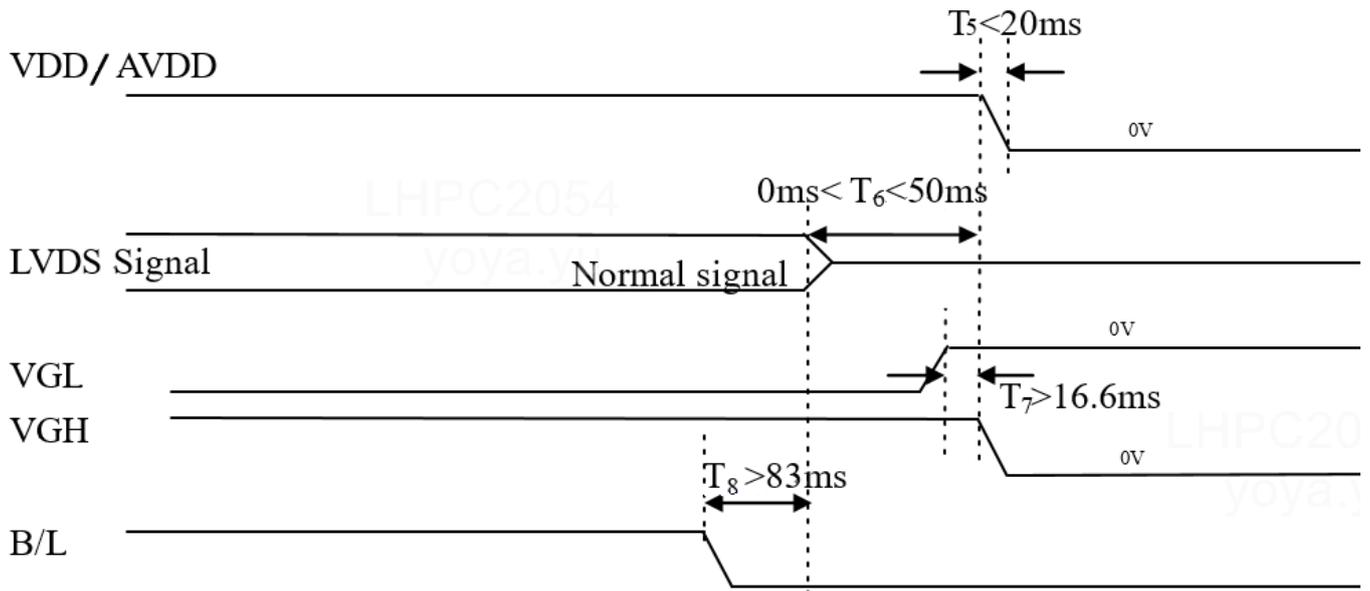
Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25°C and IL=200mA
 Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and IL=200mA. The LED lifetime could be decreased if operating IL is larger than 200mA.

3.4. Power Sequence

a. Power on:



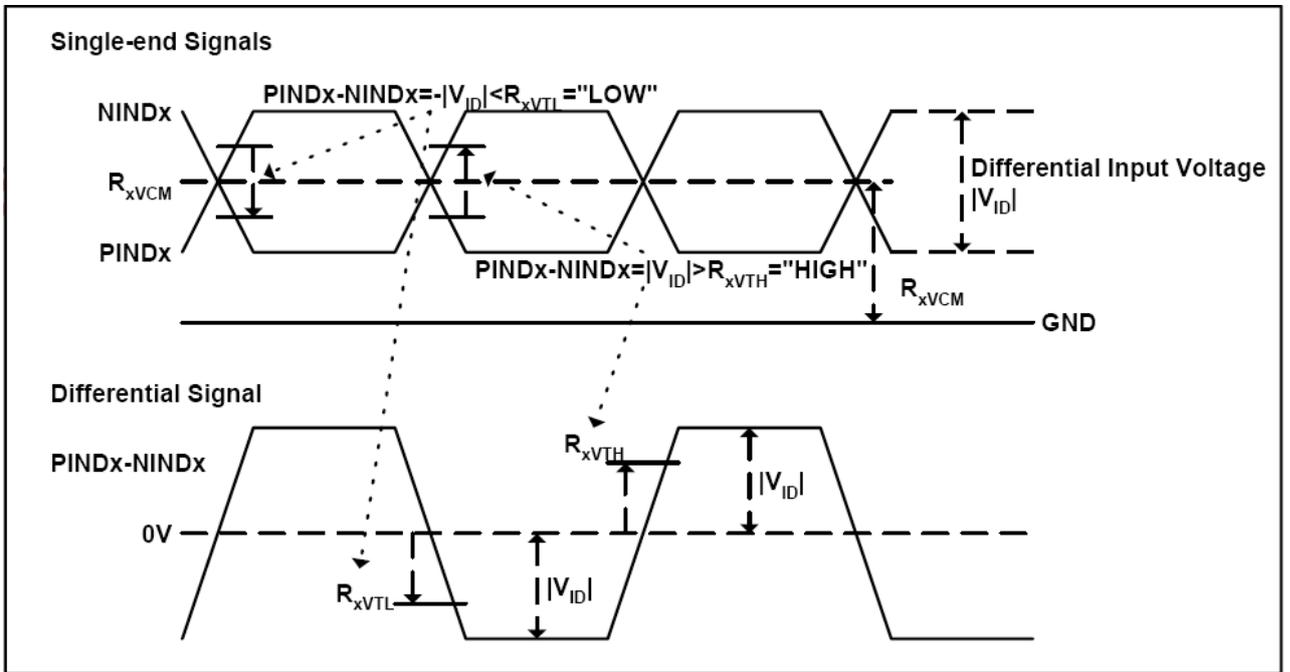
b. Power off:



3.5. LVDS Signal Timing Characteristics

3.4.1. AC Electrical Characteristics

Parameter	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
LVDS Differential input high Threshold voltage	R_{xVTH}	-	-	+200	mV	$R_{xVCM}=1.2V$
LVDS Differential input low Threshold voltage	R_{xVTL}	-200	-	-	mV	
LVDS Differential input common mode voltage	R_{xVCM}	1	1.2	$1.7 - V_{ID} /2$	V	
LVDS Differential voltage	$ V_{ID} $	200	-	600	mV	

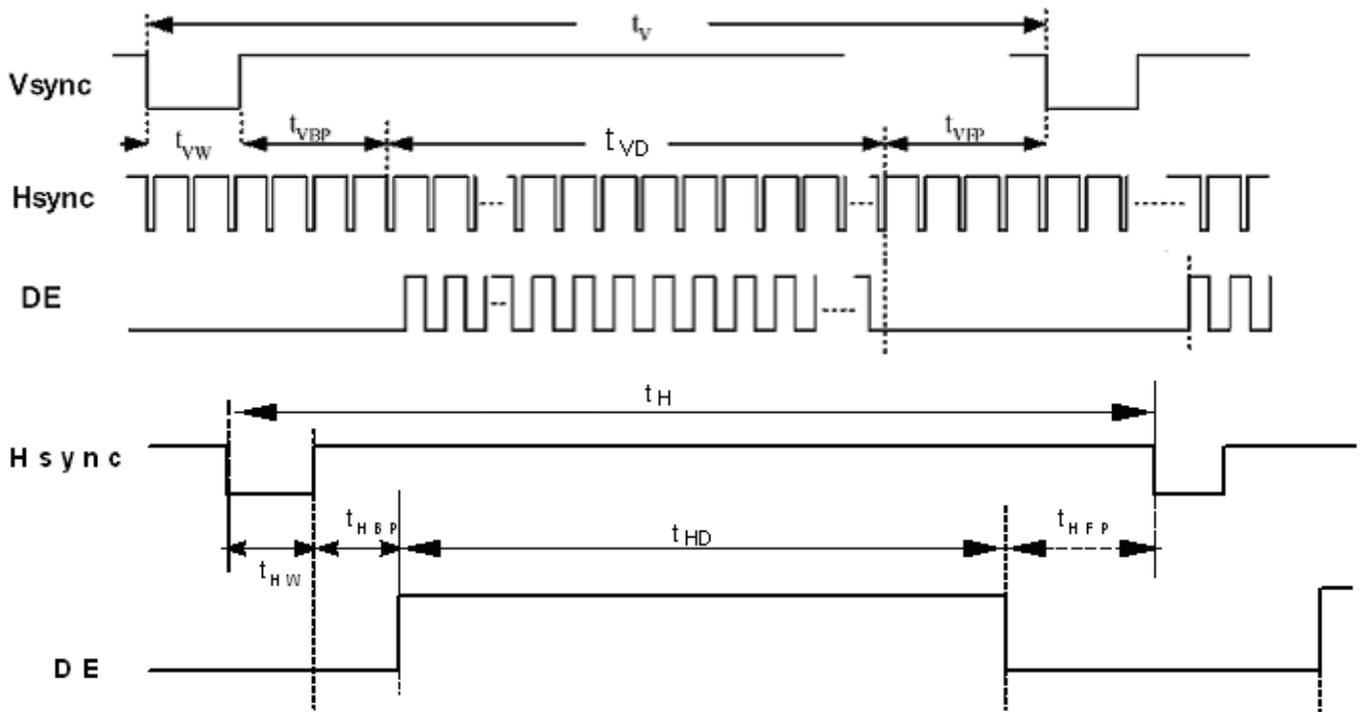


3.4.2. Timing Table

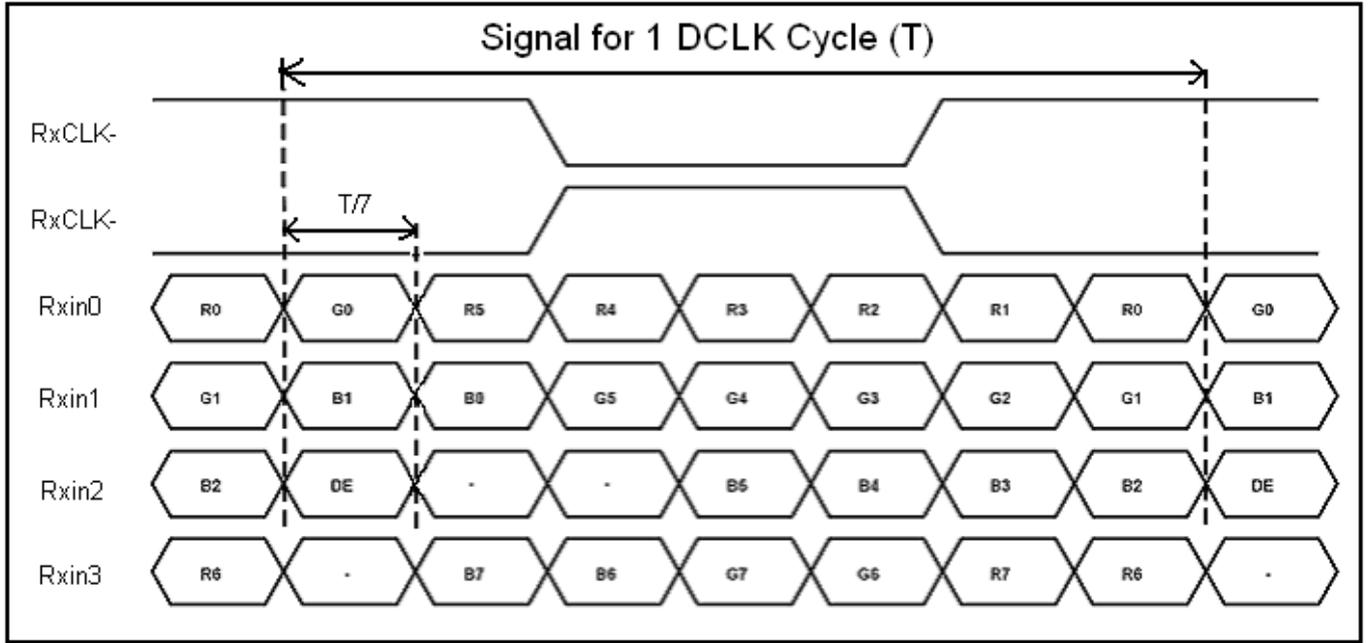
Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Clock Frequency	1/Tc	63.4	64.5	85.0	MHz	Frame rate =60Hz
Horizontal display area	tHD	1280				
HS period time	tH	1309	1322	1664	Tc	
HS Width +Back Porch +Front Porch	tHW+ tHBP +tHFP	29	42	384	Tc	
Vertical display area	tVD	800				
VS period time	tV	807	813	1040	tH	
VS Width +Back Porch +Front Porch	tVW+ tVBP +tVFP	7	13	240	tH	

Note1: Frame rate is 60 ± 5 Hz, PCLK=Vtotal*Htotal*Frame Rate;

Note2: All the above conditions must be met.



3.4.3. LVDS Data Input Format



4. Optical Specifications

Item	Symbol	Condition	Values			Unit	Remark
			Min.	Typ.	Max.		
Viewing angle (Cr ≥ 10)	θ_L	$\Phi = 180^\circ$ (9 o'clock)	75	85	-	degree	Note 1
	θ_R	$\Phi = 0^\circ$ (3 o'clock)	75	85	-		
	θ_T	$\Phi = 90^\circ$ (12 o'clock)	75	85	-		
	θ_B	$\Phi = 270^\circ$ (6 o'clock)	75	85	-		
Response time	T_r	Normal $\theta = \Phi = 0^\circ$	-	10	20	msec	Note 3
	T_f		-	15	30	msec	Note 3
Contrast ratio	C_R		600	800	-	-	Note 4
Color chromaticity	W_X		0.24	0.29	0.33	-	Note 2 Note 5
	W_Y		0.28	0.30	0.37	-	Note 6
Luminance	L		280	350	-	cd/m ²	Note 6
Luminance uniformity	Y_U		75	-	-	%	Note 7

Test Conditions:

1. VDD=2.5V, the ambient temperature is 25°C.
2. The test systems refer to Note 2.

Note 1: Definition of viewing angle range

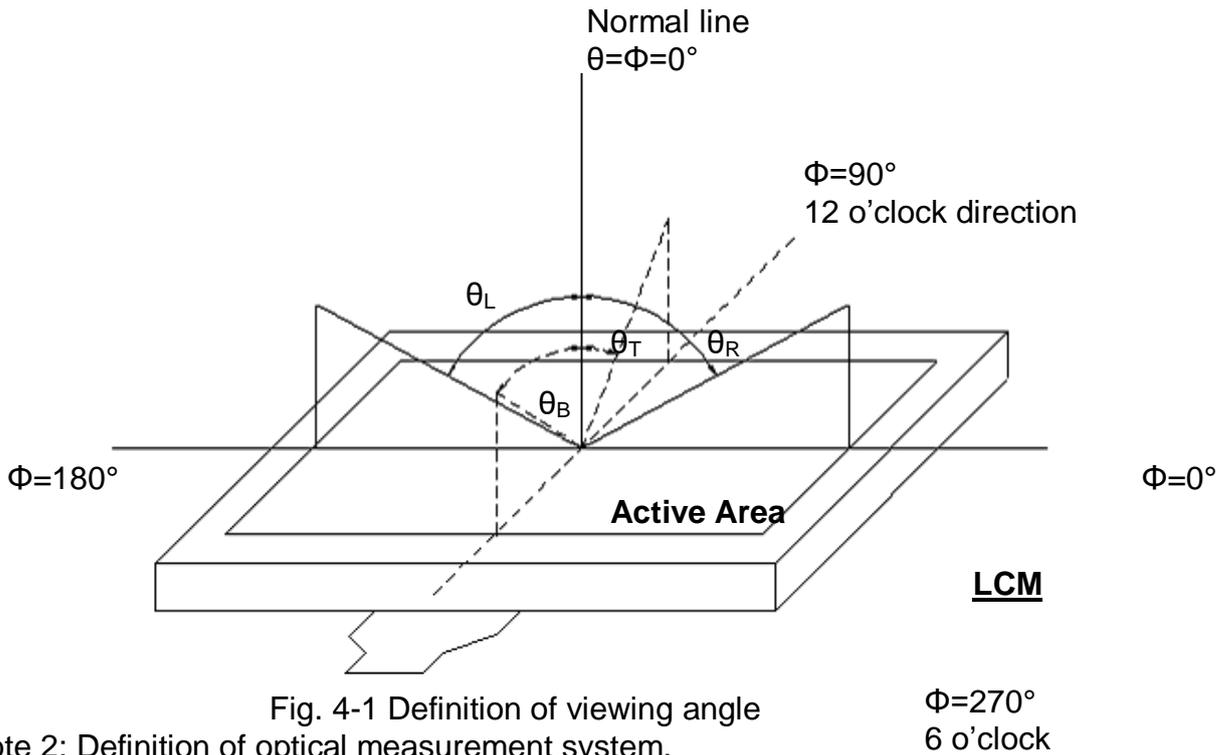


Fig. 4-1 Definition of viewing angle

Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Viewing angle is measured by ELDIM-EZ contrast/Height :1.2mm, Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/ Field of view: 1° /Height: 500mm.)

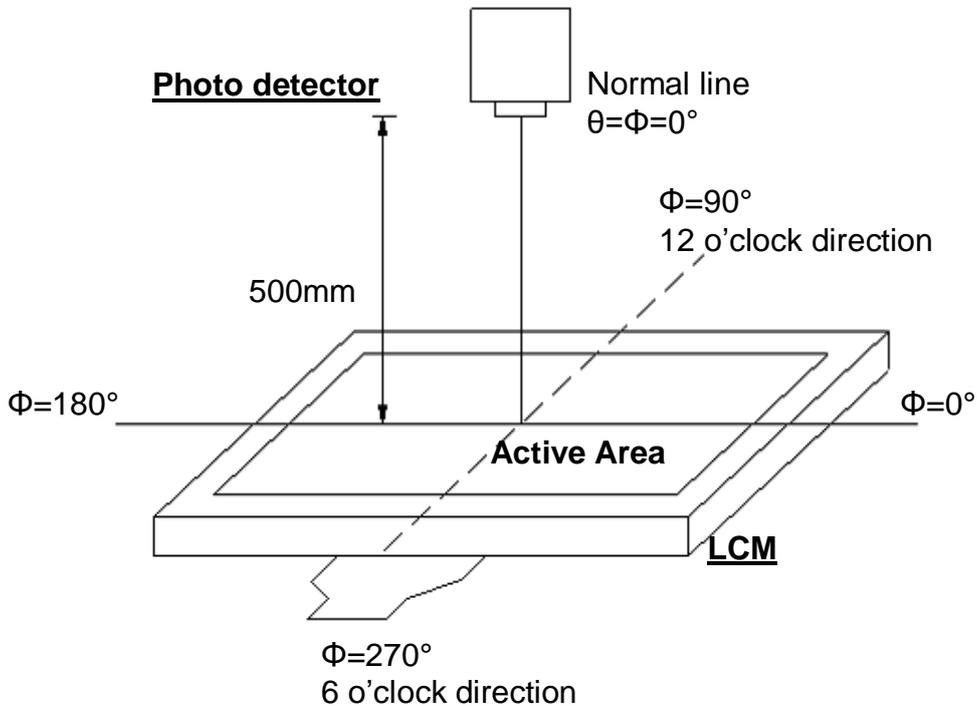


Fig. 4-2 Optical measurement system setup

Note 3: Definition of Response time

The output signals of photo detector are measured when the input signals are changed from “black” to “white”(Voltage rising time) and from “white” to “black”(Voltage falling time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.

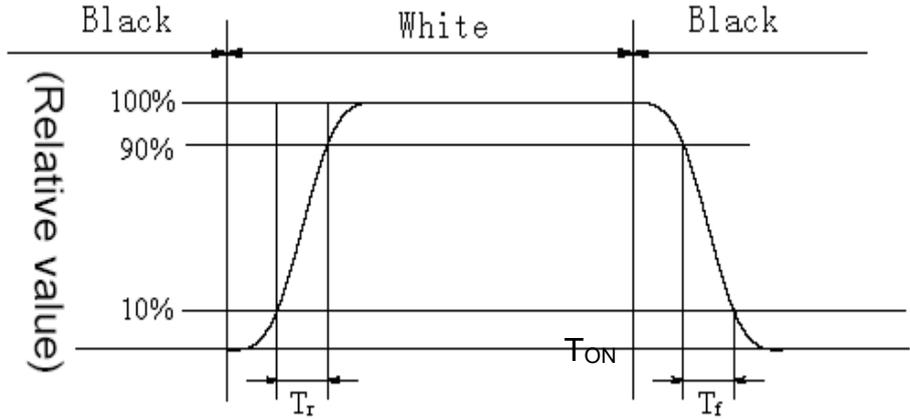


Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

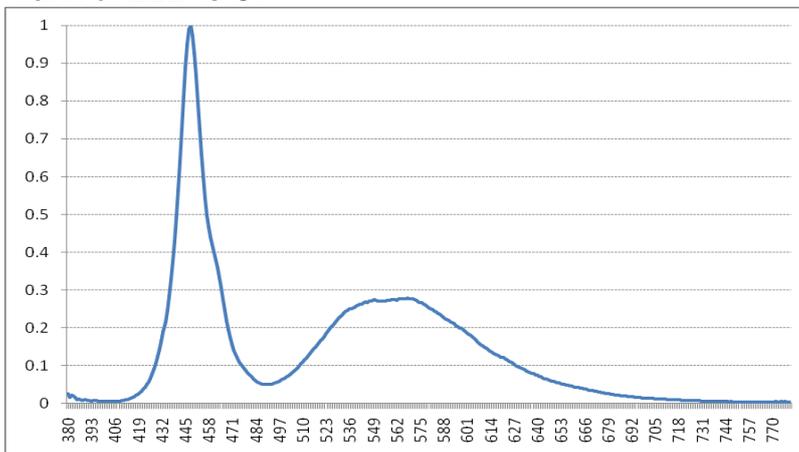
$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is $I_L=200\text{mA}$.

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened. Base on the following INX's BLU spectrum data evaluation. The backlight structure of LCM is Diffuser+ BEF +BEF, and the color rank of LED is C.



Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4).Every measuring point is placed at the center of each measuring area.

$$LuminanceUniformity(Yu) = \frac{B_{min}}{B_{max}}$$

L-----Active area length W----- Active area width

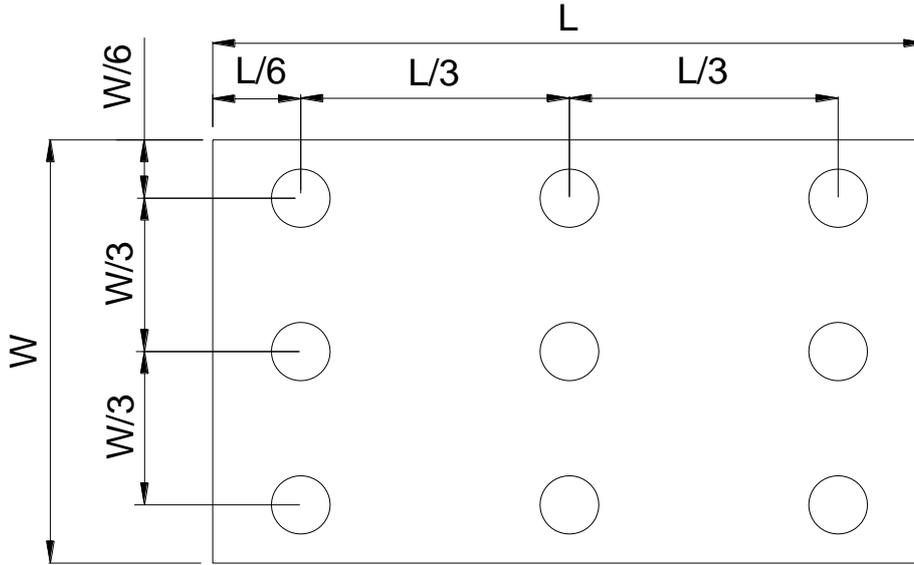


Fig. 4-4 Definition of measuring points

B_{max}: The measured maximum luminance of all measurement position.

B_{min}: The measured minimum luminance of all measurement position.

5. Reliability Test Items

6. No.	Test Item	Test Condition	Check Time
1	High Temp Storage	Ta= 70 °C	240 hrs
2	Low Temp Storage	Ta= -20 °C	240 hrs
3	High Temp Operation	Ta= 60 °C	240 hrs
4	Low Temp Operation	Ta= -10 °C	240 hrs
5	High Temp & High Humidity Operation	Ta=60°C H=90%RH	240 hrs

Note:

- (1)The test samples have recovery time need more than 2 hours at room temperature before the function check. In the standard conditions, there is no abnormal display function occurred.
- (2)After the reliability test, the product only guarantees operation function, but don't guarantee all of the cosmetic specification.
- (3)Under no condensation of dew.

7. General Precautions

7.4. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

7.5. Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

7.6. Static Electricity

1. Be sure to ground module before turning on power or operating module.
2. Do not apply voltage which exceeds the absolute maximum rating value.

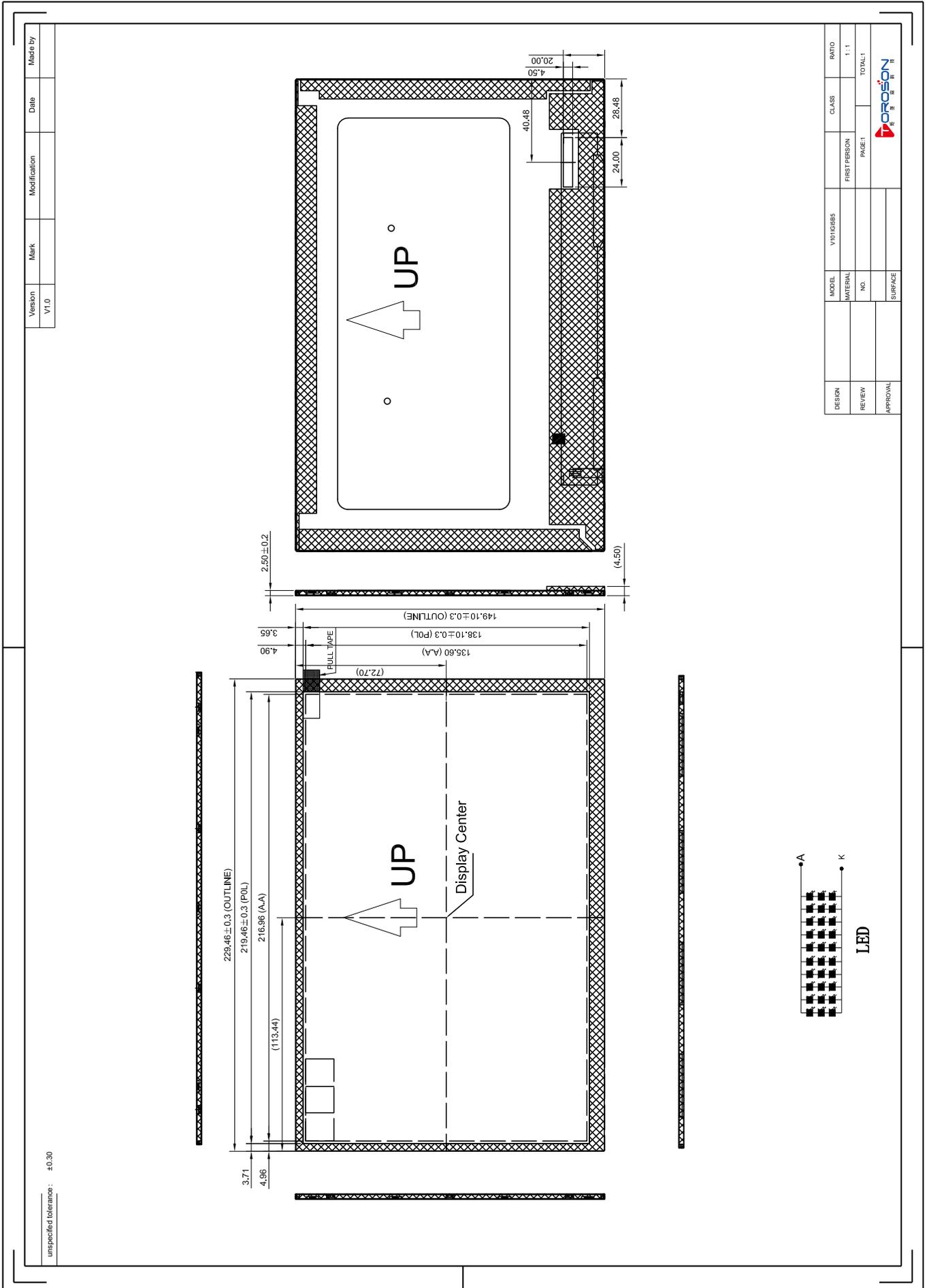
7.7. Storage

1. Store the module in a dark room where must keep at $25\pm 10^{\circ}\text{C}$ and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.

7.8. Cleaning

1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

8. Mechanical Drawing



Version	Mark	Modification	Date	Made by
V1.0				

DESIGN	MODEL	VI101GI5B5	CLASS	RATIO
REVIEW	MATERIAL NO.		FIRST PERSON PAGE:1	1:1
APPROVAL	SURFACE		TOTAL:1	



9. Package Drawing

